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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Alfred Haeusler et al. Art Unit 2812

Serial No.: 10/824,745 Examiner: Anya, Igwe U

Filed: 04/15/2004 Docket No.: TID-34858

Customer No.: 23494 Conf. No.: 7054

For: METHOD OF FABRICATING AN INTEGRATED SILICON-GERMANIUM  
HETEROBIPOLAR TRANSISTOR AND AN INTEGRATED SILICON-GERMANIUM  
HETEROBIPOLAR TRANSISTOR

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<b>NAME OF INVENTOR(S):</b> <b>Haeusler</b>	
<b>TITLE OF INVENTION:</b> <b>Method of Fabricating . . .</b>	
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